

This Page Is Inserted by IFW Operations
and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

IMAGES ARE BEST AVAILABLE COPY.

**As rescanning documents *will not* correct images,
please do not report the images to the
Image Problems Mailbox.**



(11) Publication number : **0 652 696 A1**

(12)

EUROPEAN PATENT APPLICATION

(21) Application number : **94308071.3**

(51) Int. Cl.⁸ : **H05K 9/00, G02B 6/42**

(22) Date of filing : **02.11.94**

(30) Priority : **09.11.93 US 149422**

(43) Date of publication of application :
10.05.95 Bulletin 95/19

(84) Designated Contracting States :
DE FR GB

(71) Applicant : **AT & T Corp.**
32 Avenue of the Americas
New York, NY 10013-2412 (US)

(72) Inventor : **Benzoni, Albert Michael**
1690 Minesite Road
Allentown, Pennsylvania 18103 (US)

(74) Representative : **Johnston, Kenneth Graham et al**
AT&T (UK) Ltd.
5 Mornington Road
Woodford Green Essex, IG8 OTU (GB)

(54) **Shielded arrangement.**

(57) A shielded member includes a nonconductive housing (e.g., 20) for mounting on a substrate (e.g., 22). The housing (e.g., 20) includes a cavity (e.g., 6) and has a base to be received against the substrate (e.g., 22). The housing includes at least one integral mounting post (e.g., 62) extending beyond the base for reception in respective aperture (e.g., 64) in the substrate. A conductive coating extends over at least a portion of the housing and at least a portion of the integral mounting post. The conductive coating provides shielding for the cavity with currents induced in the conductive coating on the housing conductible to a trace (e.g., 82) on the substrate by way of the conductive coating on the integral mounting post.

EP 0 652 696 A1

This invention relates to a shielded arrangement such as a shielded member, a shielded transceiver or a shield housing for an electro-optical circuit.

Prior art metal housings for optical transceivers have been cast, molded, formed, or machined from a variety of alloys, such as an aluminum alloy. Some metal mounting posts were press inserted or threaded into holes in the housings. Other metal housings had mounting posts that were integrally machined or cast oversized and machined in a secondary operation. It has been found during wave soldering of electrical leads and the mounting posts to traces on a substrate, that the metal housing acts as a large heat sink with respect to the mounting posts. This has caused cold solder joints to form at some mounting posts. The cold solder joints decrease the reliability of a circuit path from the housing to an electrically conductive trace on the substrate on which the housing is mounted, and reduce the retention force of the solder joints that secure the housing to the substrate. Concomitantly, a failed solder joint will reduce the shielding effectiveness of the housing by including a high impedance in the path between the conductive housing and an electrically conductive trace on the substrate.

Prior art plastic housings for optical transceivers have included insert molded, ultrasonically bonded, or threaded, screw inserted metal mounting posts. The ultrasonically bonded mounting posts were typically plated, along with the housing, after insertion. The threaded screw inserted mounting posts were typically mounted into the housing after both the housing and the mounting posts were individually plated. Inserting the mounting post subsequent to both the housing and mounting posts being individually plated has caused the plating to crack.

The above problems, coupled with tolerance requirements on the positioning of the mounting posts, requires that each housing be inspected after manufacture. This increases the cost of manufacturing housings for optical transceivers. A need remains in the art for a cost effective technique for providing shielding to a housing while simultaneously providing a reliable low impedance path to conduct electrostatic energy or energy induced by electromagnetic radiation to an electrically conductive trace.

Summary of the Invention

In accordance with one aspect of the present invention, a shielded member includes a nonconductive housing for mounting on a substrate having a conductive trace thereon. The housing includes a cavity and has a base adapted to be positioned against the substrate. The housing includes one or more integral mounting posts extending beyond the base for reception in respective apertures in the substrate. A conductive coating extends over at least a portion of the

housing and at least a portion of one or more integral mounting post. The conductive coating provides shielding for devices the cavity, with currents induced in the conductive coating on the housing being conductible to an electrically conductive on the substrate by way of the conductive coating on the integral mounting post.

Brief Description of the Drawing

The invention will now be described by way of example in which:

Figure 1 is an exploded perspective view of a housing for an optical transceiver, in accordance with an illustrative embodiment of the present invention, positioned over a substrate on which the transceiver is adapted to be mounted;

Figure 2 is a side view of the transceiver of Figure 1, having a receptacle secured thereto and a pair of optical cables coupled to the transceiver, with the leads and mounting posts soldered to traces on a substrate;

Figure 3 is an enlarged side view of an alternate embodiment mounting post providing a friction fit; and

Figure 4 is an enlarged side view of yet another alternate embodiment mounting post providing a bayonet latch.

Detailed Description

Figure 1 is an exploded perspective view of a portion of an optical transceiver including an illustrative embodiment of the invention showing of a housing 20 positioned over a substrate 22, such as a substrate, on which it is adapted to be mounted. Lid 24 is positioned above the housing. Housing 20 and lid 24 are preferably molded of a nonconductive material. Nonconductive means the material has an electrically insulating matrix. The electrically insulating matrix that may or may not contain electrically conductive particles. The level of measurable conductivity of the material, as measured through volume resistivity, is proportional to the density and interconnection of the electrically conductive particles in the electrically insulating matrix. When the material contains electrically conductive particles, the shielding effectiveness may not be sufficient to effectively shield electrically sensitive circuits or to block emissions from highly radiating circuits. Preferably housing 20 and lid 24 are molded of a platable engineering grade plastic. The plastic is preferably a high temperature plastic such as polythiophene sulfone, polyetherimide, or a liquid crystal polymer.

Housing 20 has side walls 26 and 28 and end walls 30 and 32 that extend upwardly from a bottom wall 34 to define a cavity 36 within housing 20. In bottom wall 34, extending along side walls 26 and 28, are

clearance slots 38 and 40 through which leads 42 extend. Ribs 44 and 46 are positioned centrally between side walls 26 and 28. They extend inwardly into cavity 36 and toward each other from end walls 30 and 32. Each rib has a channel extending away from bottom wall 34. These channels, 48 and 50, are each adapted to receive an end of the receiver-transmitter lid shield 52. End wall 32 also includes two U-joints 54 and 56 for joining receptacle 58 (see figure 2). Extending downward from the lower surface of bottom wall 34 at corners of housing 20 are standoffs 60. The lower surface of standoffs 60 define a plane and engage the upper surface of a substrate 22 on which housing 20 is mounted. The standoffs provide a gap between the lower surface of bottom wall 34 and the top surface of the substrate. This gap allows the surface of the substrate to be washed subsequent to soldering the housing onto the substrate.

Extending downwardly from each standoff is a mounting post 62. Mounting posts 62 are integrally molded with housing 20. In the illustrative embodiment, mounting posts 62 are cylindrical in cross section and smaller in diameter than apertures 64 in substrate 22 in which the mounting posts are adapted to be received. Typically mounting posts 62 extend beyond the standoff a greater distance than leads 42 and have a tapered or radiused distal end 66 to facilitate insertion into apertures 64. They also typically extend beyond standoffs 60 a greater distance than leads 42. This arrangement causes leads 42 to be aligned with plated through holes 68 when mounting posts 62 are aligned with and are inserted into apertures 64.

Lid 24 is planar with lid shield 52 extending from an inner surface. Gusset plates 70 extend from the under surface of lid 24 to shield 52 to support the lid shield and maintain lid 24 flat. Lid shield 52 is sized to have ends received in channels 48 and 50. When lid 24 is positioned on the housing with ends of the lid shield received in the channels, lid shield 24 and ribs 44 and 46 divide cavity 36 into two portions. A transmitter 72 is housed in the first portion of cavity 36 and is coupled through U-joint 54 to optical interface 74 (see figure 2). A receiver 76 is housed in the second portion of cavity 36 and is coupled to optical interface 78 (see figure 2) through U-joint 56. Transmitter 72 and receiver 76 are well known in the art. Although the transmitter 72 and receiver 76 are shown mounted on a single substrate, they could be mounted on separate substrates. When the transmitter and receiver are mounted on separate substrates, lid shield 52 may extend between the substrates.

To provide shielding, surfaces of housing 20 and lid 24 are uniformly plated with an electrically conductive material, such as copper, in an electroless plating process, as is known in the art. During the plating process, housing 20 and lid 24 may be held at locations that minimize the loss of effective shielding,

such as on surfaces of channels 48 and 50 of housing 20 and on gusset plates 70 of lid 24. The conductive layer provides a low resistance electrically conductive coating that effectively blocks, or shields, electrostatic and electromagnetic energy from passing through the housing, lid, or lid shield. A layer of another material, such as nickel, can be deposited over the conductive layer to prevent oxidation and scratching.

Lid shield 52 prevents digital switching noise in the form of electromagnetic radiation generated in the first portion of cavity 36 by transmitter 72 from passing into the second portion of cavity 36. It is undesirable to have electromagnetic radiation induced in the conductors and leads of components, such as integrated circuits, in the more sensitive receiver 76. The receiver is more sensitive to electromagnetic radiation than the transmitter because it operates at very low current levels and with very large amplification factors.

Lid 24 is secured to housing 20 by a conductive medium, such as conductive epoxy or solder, to provide continuity in a low resistance electrical path from the conductive coating on the lid to the conductive coating on the housing. Lid 24, when received on housing 20, effectively encloses cavity 36 and the optical-electric transducers therein within shielded members.

While electroless plating provides a coating over all surfaces of the housing and lid, masking portions of the surfaces with a stop-off paint prior to plating would result in selective plating. All surfaces of the housing and lid do not have to be plated to provide effective shielding. For example, it would not be necessary to plate both the inside and outside surfaces of the housing, or to plate both the inner and outer surfaces of the lid. Gaps in the shielding do not diminish shielding effectiveness as long as the gaps are smaller than one-half of the shortest wavelength of any frequency of interest, as is known in the art.

With the housing mounted on substrate 22, any currents induced in the conductive coating on the housing or lid can be conducted through the conductive coating on the integral mounting posts to an electrically conductive trace 82 on the substrate. Posts 62 may be soldered, such as at 80 (see figure 2), to the conductive trace to provide a low impedance path from the conductive coating on the integral mounting posts to the conductive trace on the circuit board. A conductive trace may be provided either on the upper or lower surface of substrate 22.

While mounting posts 62 in the illustrative embodiment of the invention are sized to pass into apertures 64 without an interference fit, the invention is not limited thereto. By way of example and not limitation, alternative embodiment mounting posts are shown in figures 3 and 4. In figure 3, alternative embodiment mounting post 90 has a dimension, d, that provides an interference fit with side walls of aper-

tures 64. Tapered ends 92 of individual members 94 and 96 cooperate with apertures 64 to facilitate entry into the apertures. Slot 98 permits the individual members 94 and 96 to flex toward each other during insertion.

Figure 4 shows a bayonet type mounting post 100. Tapered ends 102 of individual members 104 and 106 cooperate with apertures 64 and substrate 22 to facilitate entry of mounting post 100 into apertures 64. Slot 108 permits individual members to flex toward each other during insertion. Surfaces 110 and 112 are spaced a distance, h, below the bottom of standoffs 60 that corresponds to the thickness of substrate 22 on which housing 20 will be mounted. Surfaces 110 and 112 latch under substrate 22 to secure housing 20 to substrate 22.

Other designs of mounting posts exist with which the invention can be utilized. Furthermore, in place of the plated copper, a conductive paint can be applied to housing 20, including integral mounting posts 62, or lid 24 to achieve similar results.

For aesthetic purposes, housing 20 and lid 24 may be masked and selectively painted after being plated. When the paint is not electrically conductive and unplatable, the paint acts as a solder mask in the region of mounting posts 62. This controls the distance along mounting posts 62 that solder is wicked during a soldering process. To facilitate soldering, mounting posts 62 may be tinned or coated with a layer of solder. Since PES has a heat deflection temperature where parts molded therefrom start to deform at 205°C, which is higher than the minimum 180°C melt temperature of 63-37 tin-lead solder, housings molded with integral mounting posts can withstand the wave solder process. Furthermore, since the housing is nonconductive, it does not act as a heat sink thereby obviating the cold solder joint problem encountered in the prior art.

While electroless plating has been described, the housing and lid could be electroplated after an initial flash layer of electroless plating. Although electroplating is faster, the thickness of plating varies compared to electroless plating due to the plating rate being voltage gradient dependent and the field being geometry dependent.

Claims

1. A shielded arrangement comprising a member including a nonconductive housing (20) for mounting on a substrate (22) having an electrically conductive trace (82), the housing defining a cavity (36) and having a base to be received against the substrate, the housing (20) having at least one integral mounting post (62) extending beyond the base for reception in an aperture (64) in the substrate, the shielded member characterized by

the housing (20) having an electrically conductive coating extending over at least a portion thereof and at least a portion of the at least one integral mounting post, whereby the conductive coating shields the cavity (36), with energy induced in the conductive coating on the housing (20) being conductible to the electrically conductive trace (82) on the substrate (22) when the housing (20) is mounted thereon by way of the conductive coating on the integral mounting post (62).

2. A shielded arrangement comprising a transceiver including a nonconductive housing (20) for mounting on a substrate (22) having an electrically conductive trace (82), the housing (20) defining a cavity (36), a receiver (76) and transmitter (72) received in the cavity (36), the housing (20) having a base to be received against the substrate, the housing having at least one integral mounting post (62) extending beyond the base for reception in an aperture (64) in the substrate, the shielded transceiver characterized by the housing having an electrically conductive coating extending over at least a portion thereof and at least a portion of the at least one integral mounting post, whereby the conductive coating provides shielding of the receiver and transmitter from electrostatic and electromagnetic energy with energy induced in the conductive coating on the housing conductible to the electrically conductive trace on the substrate when the shielded transceiver is mounted thereon by way of the conductive coating on the integral mounting post.
3. A shielded arrangement comprising a housing for an electro-optic circuit, including a nonconductive housing (20) for mounting on a substrate (22) having an electrically conductive trace (82), the housing having a cavity (36), an electro-optic circuit (72,76) received in the cavity (36), the housing having a base to be received against the substrate and an integral mounting post (62) extending beyond the base for reception in an aperture (64) in the substrate, the shielded housing characterized by the housing having an electrically conductive coating extending over at least a portion of the housing and at least a portion of the integral mounting post, whereby the conductive coating shields the electro-optic circuit from electrostatic or electromagnetic energy with any energy induced in the conductive coating on the housing conductible to an electrically conductive trace on the substrate by way of the conductive coating on the integral mounting post.
4. A shielded arrangement as recited in claim 1, 2 or 3, further characterized in that the nonconductive

housing (20) is molded of a material containing electrically conductive particles.

5. A shielded arrangement as recited in claim 1, 2 or 3, further characterized in that the nonconductive housing (20) and integral mounting post (62) are molded. 5
6. A shielded arrangement as recited in claim 5, further characterized in that the housing (20) and integral mounting post (62) are molded of a material selected from polyether sulfone, polyetherimide, and liquid crystal polymer. 10
7. A shielded arrangement as recited in claim 1, 2 or 3, further characterized in that the electrically conductive coating is a metal (for example copper) applied by electroless plating, or as a conductive paint. 15
8. A shielded arrangement as recited in claim 1, 2 or 3, further characterized by a lid (24), having an electrically conductive coating, the conductive coating on the lid electrically commoned with the conductive coating extending over at least a portion of the housing (20). 20
9. A shielded arrangement as recited in claim 8, wherein the lid is further characterized by a partition member (52) extending into the cavity (36), the partition member (52) having a conductive coating, the partition member (52) dividing the cavity (36) into a first portion and a second portion, whereby emissions generated in one of the portions are prevented from entering the other portion. 25
10. A shielded arrangement as recited in claim 8, further characterized in that the lid further comprises a partition member (52) extending into the cavity (36), the partition member having a conductive coating, the partition member dividing the cavity into a first portion receiving the transmitter (72) and a second portion receiving the receiver (76), whereby digital switching noise in the form of electromagnetic interference generated by the transmitter is prevented from radiating into the second portion of the cavity. 30
11. A shielded arrangement as recited in claim 8, for an electro-optic circuit, further characterized in that the lid (24) further comprises a partition member (52) extending into the cavity (36), the partition member having a conductive coating, the partition member dividing the cavity into a first portion for receiving a first portion of the electro-optic circuit (72) and a second portion for receiving a second portion of the electro-optic circuit (76), whereby digital switching noise generated by the first portion of the electro-optic circuit is prevented from radiating into the second portion of the cavity. 35

cuit (76), whereby digital switching noise generated by the first portion of the electro-optic circuit is prevented from radiating into the second portion of the cavity.

12. A shielded member as recited in claim 1, further characterized by coating that is not electrically conductive extending over at least a portion of the conductive coating, the nonconductive coating capable of providing a mask to limit the flow of solder when the shielded member is mounted on and soldered to the substrate. 40
13. A shielded member as recited in claim 1, further characterized in that the at least one integral mounting post (62) extends from the base of the housing, or a solder coating extends over the conductive coating on the mounting post. 45

FIG. 1

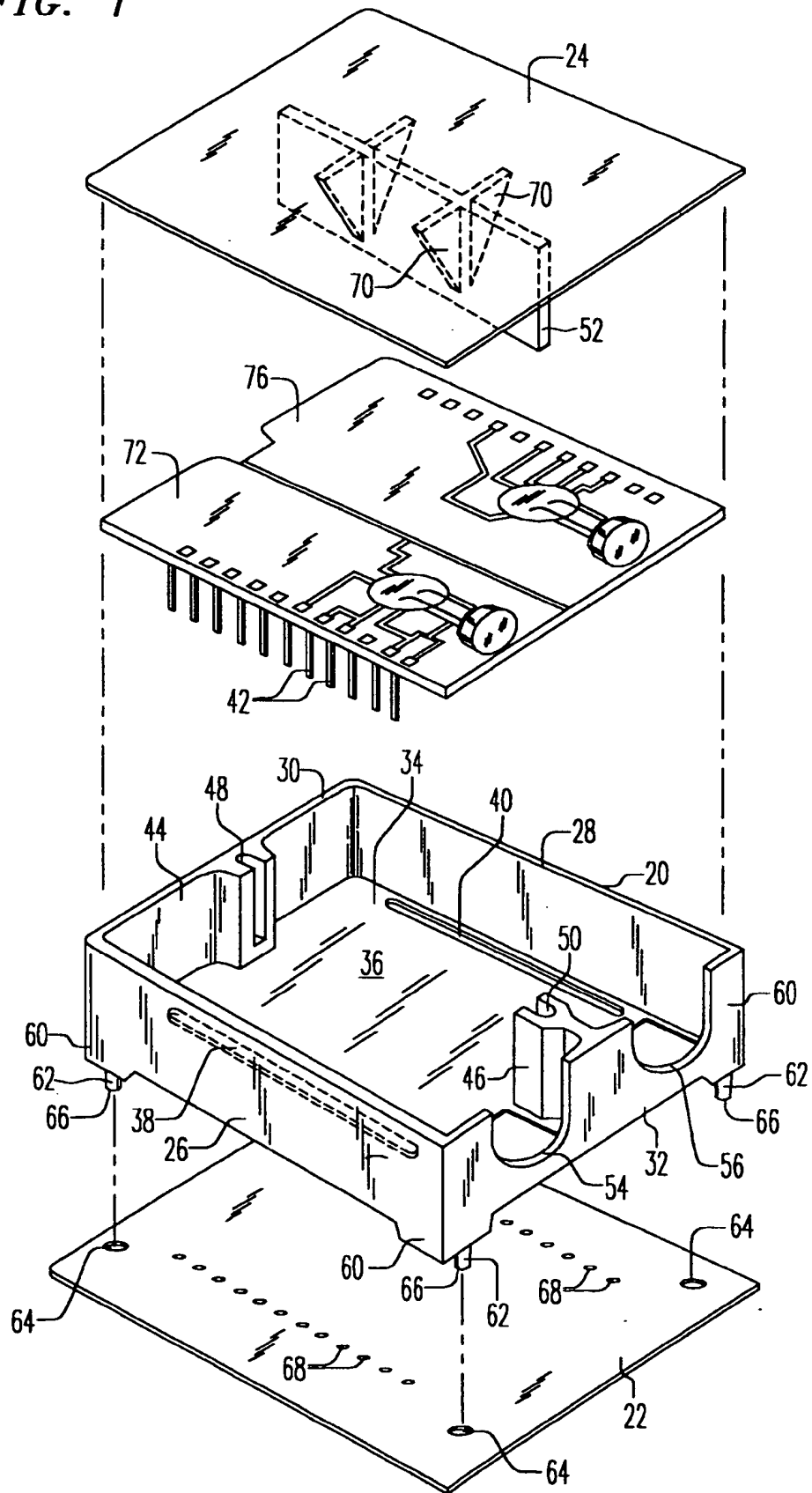


FIG. 2

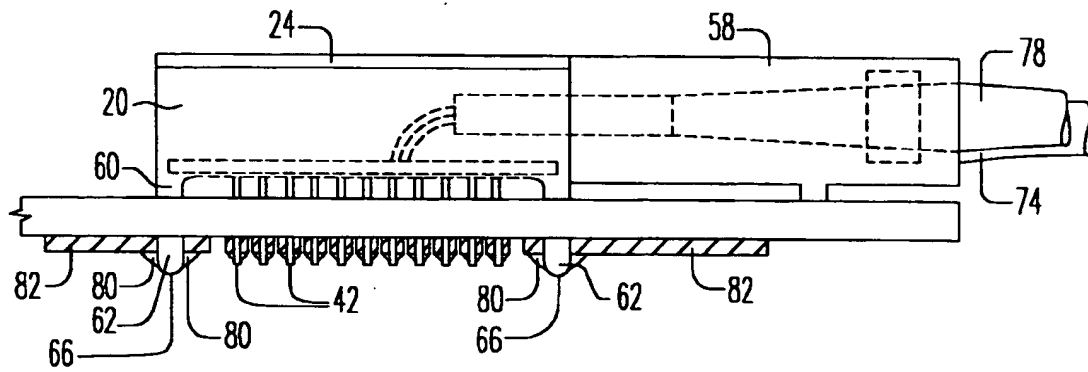


FIG. 3

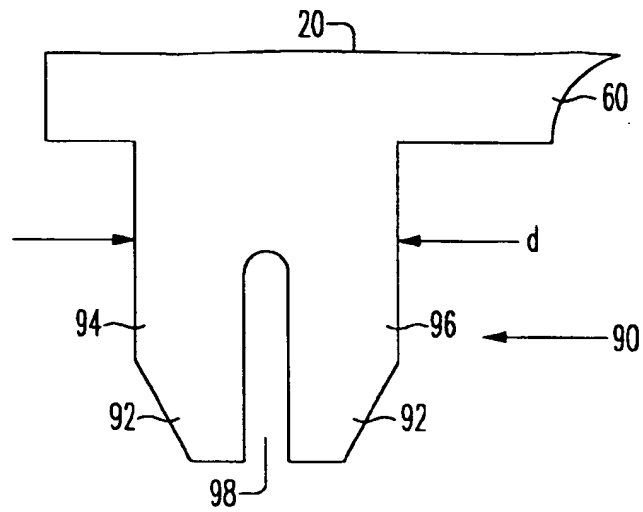
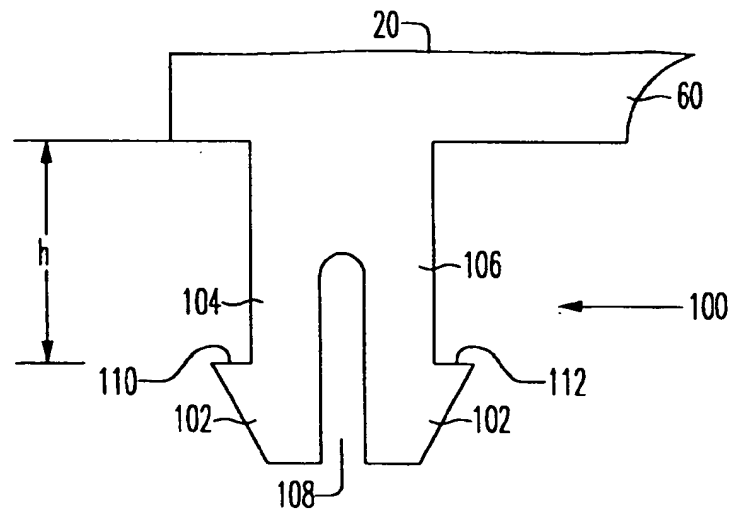


FIG. 4





European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 94 30 8071

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
Y	WO-A-90 14607 (E.I. DU PONT DE NEMOURS AND COMPANY) * the whole document * ---	1-3	H05K9/00 G02B6/42
Y	DE-B-12 57 233 (BLAUPUNKT-WERKE G.M.B.H.) * the whole document * ---	1-3	
A	DE-U-86 04 884 (VALENTIN) * the whole document * ---	4-8	
A	EP-A-0 437 931 (AMERICAN TELEPHONE AND TELEGRAPH COMPANY) * the whole document * ---	1-3	
A	US-A-4 618 915 (BURY) * the whole document * -----	1-3	
The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int.Cl.6) H05K G02B
Place of search THE HAGUE		Date of completion of the search 27 February 1995	Examiner Toussaint, F
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

EPO FORM 1503 (03.92) (P04C01)